

**What is claimed is:**

1. A Pixel Circuit For Liquid Crystal Display Using Static Memory for lowering power consumption via combining an analogue and a digital circuit, the circuit comprises:
  - a plurality of multiplexers, acting as switching elements for performing a plurality of output voltage transforming functions;
  - a static memory, connecting to a scanning line, a thin film transistor and a capacitor, for storing the digital voltage signals stored in the capacitor;
  - a thin film transistor, for connecting a scanning line and a data line, acting as a control switch of the circuit; and
  - a capacitor, connecting to the thin film transistor, where analogue or digital signals from the data line are stored.
2. The Pixel Circuit For Liquid Crystal Display Using Static Memory of claim 1, wherein said plurality of multiplexers comprises a first multiplexer and a second multiplexer.
3. The Pixel Circuit For Liquid Crystal Display Using Static Memory of claim 1, wherein said first multiplexer further comprises a general voltage terminal and a reference voltage terminal.

4. The Pixel Circuit For Liquid Crystal Display Using Static Memory of claim 1, wherein one terminal of the static memory further connects to the first multiplexer, the other terminal of static memory connects to the thin film transistor and the capacitor.
5. The Pixel Circuit For Liquid Crystal Display Using Static Memory of claim 1, wherein the second multiplexer further comprises:
  - a selection terminal;
  - an output terminal;
  - a first mode terminal; and
  - a second mode terminal.
6. The Pixel Circuit For Liquid Crystal Display Using Static Memory of claim 5, wherein said second multiplexer further connects to a mode terminal.
7. The Pixel Circuit For Liquid Crystal Display Using Static Memory of claim 5, wherein said output terminal further connects to a liquid crystal unit.
8. The Pixel Circuit For Liquid Crystal Display Using Static Memory of claim 5, wherein said first mode terminal further connects to the capacitor and the thin film transistor.

9. The Pixel Circuit For Liquid Crystal Display Using Static Memory of claim 5, wherein the second mode terminal further connects to the first multiplexer.

10. A Pixel Circuit For Liquid Crystal Display Using Static Memory for lowering power consumption via combining an analogue and a digital circuit, the circuit comprises:

a first multiplexer, having a plurality of current switching elements;

a demultiplexer, having a plurality of current switching elements;

a static memory, connecting to a scanning line connected to the first multiplexer and the demultiplexer, for storing the digital voltage signals stored in the demultiplexer;

a thin film transistor, for connecting a scanning line and a data line, acting as a control switch of the circuit;

a capacitor, connecting to the thin film transistor, where analogue or digital signals from the data line are stored; and

a second switch device, connecting to the first multiplexer, the demultiplexer and a liquid crystal unit.

11. The Pixel Circuit For Liquid Crystal Display Using Static Memory of claim 10, wherein said first multiplexer further

comprises a general voltage terminal and a reference voltage terminal.

12. The Pixel Circuit For Liquid Crystal Display Using Static Memory of claim 10, wherein the second switch device connects to a mode terminal via a second signal line for controlling switch signals.

13. The Pixel Circuit For Liquid Crystal Display Using Static Memory of claim 10, wherein the demultiplexer further comprises:

a selection terminal;

a demultiplexer input terminal;

a first mode terminal; and

a second mode terminal.

14. The Pixel Circuit For Liquid Crystal Display Using Static Memory of claim 13, wherein said demultiplexer further connects to a mode terminal.

15. The Pixel Circuit For Liquid Crystal Display Using Static Memory of claim 13, wherein the input terminal of the demultiplexer further connects to the thin film transistor.

16. The Pixel Circuit For Liquid Crystal Display Using Static

Memory of claim 13, wherein the output terminal of the first mode terminal further connects to the second switch device.

17. The Pixel Circuit For Liquid Crystal Display Using Static Memory of claim 13, wherein the output terminal of the second mode terminal further connects to the static memory.